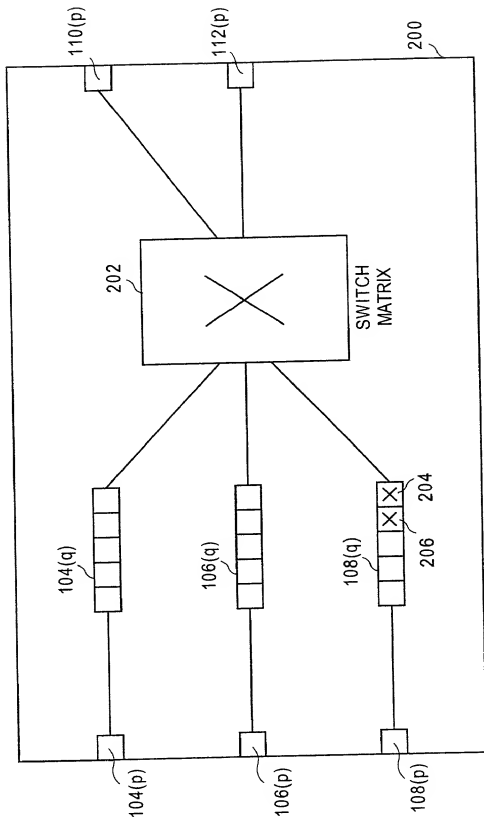


FIG. 1  
(prior art)



INPUT BUFFER SWITCH

FIG. 2A  
(prior art)

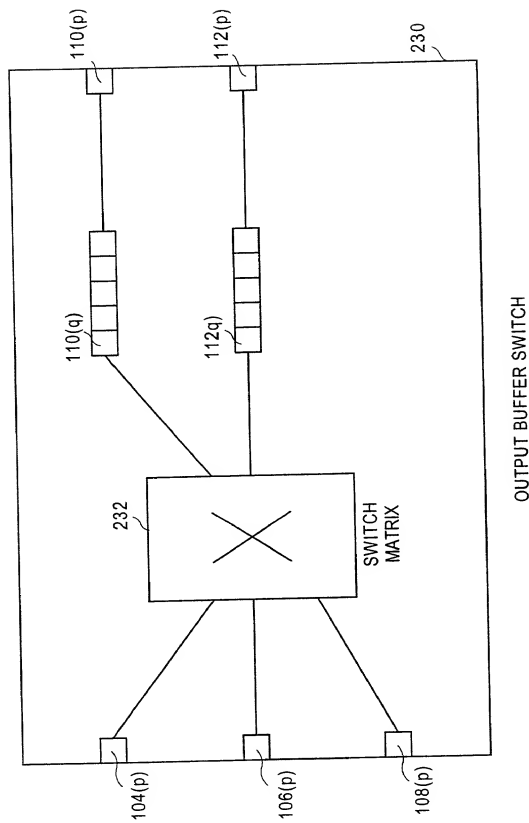
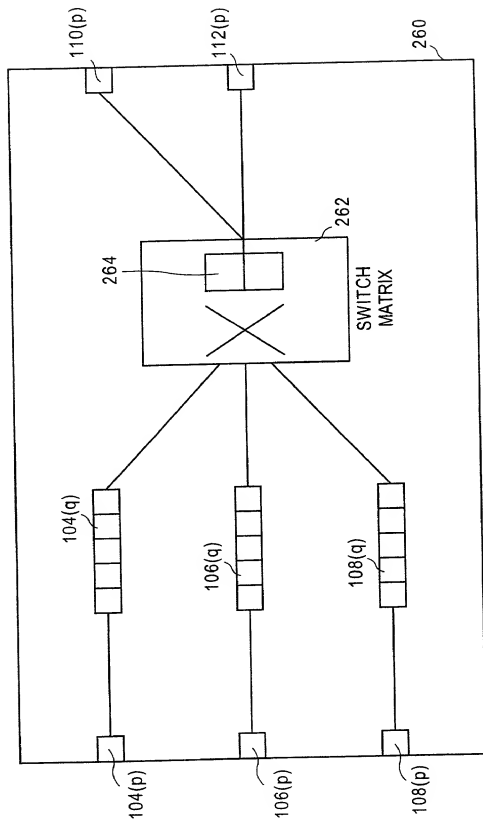


FIG. 2B  
(prior art)



HYBRID SWITCH

FIG. 2C  
(prior art)

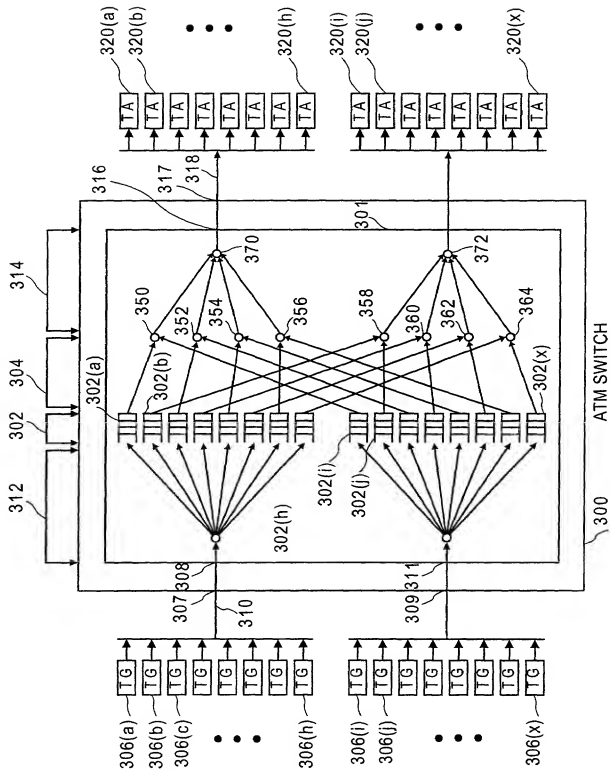


FIG. 3

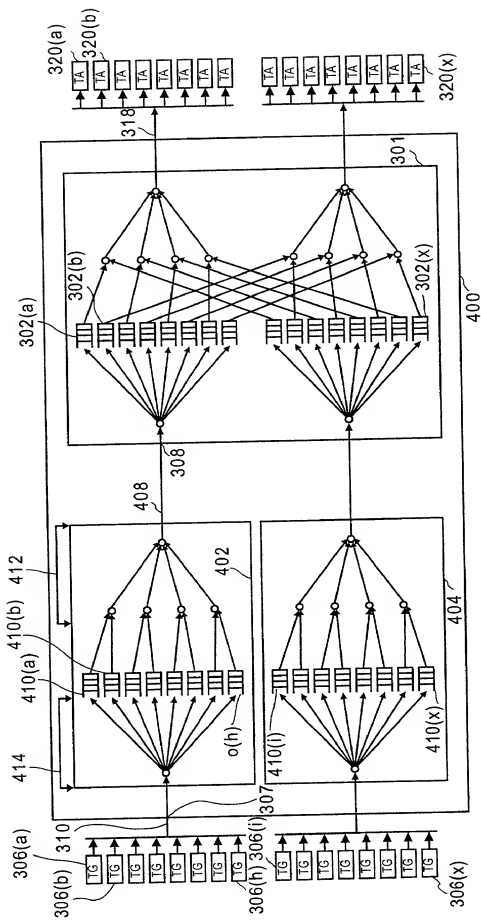


FIG. 4

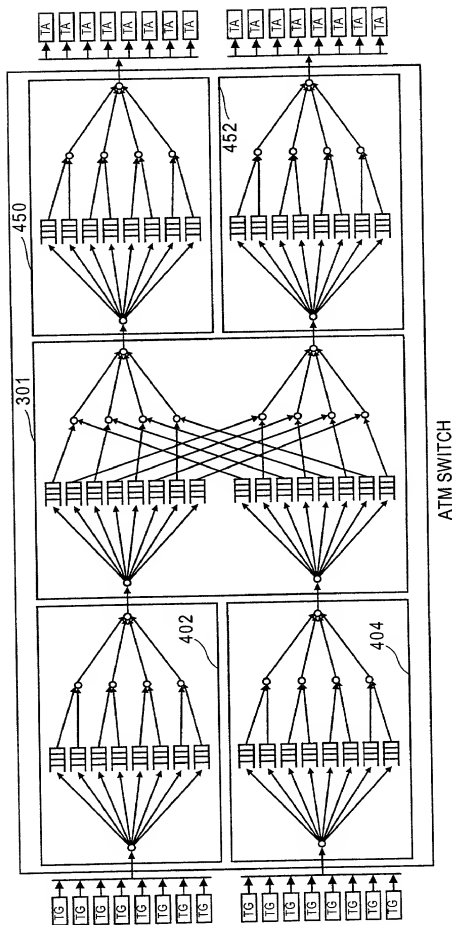


FIG. 5

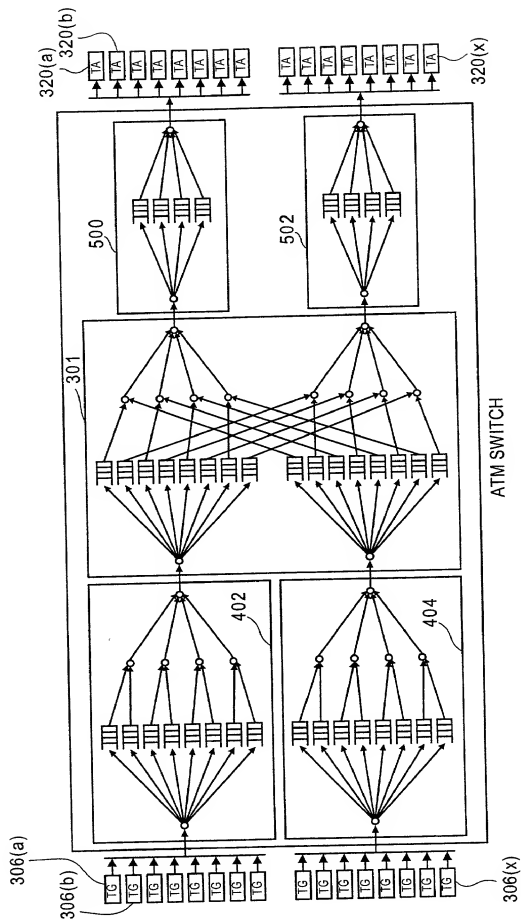


FIG. 6



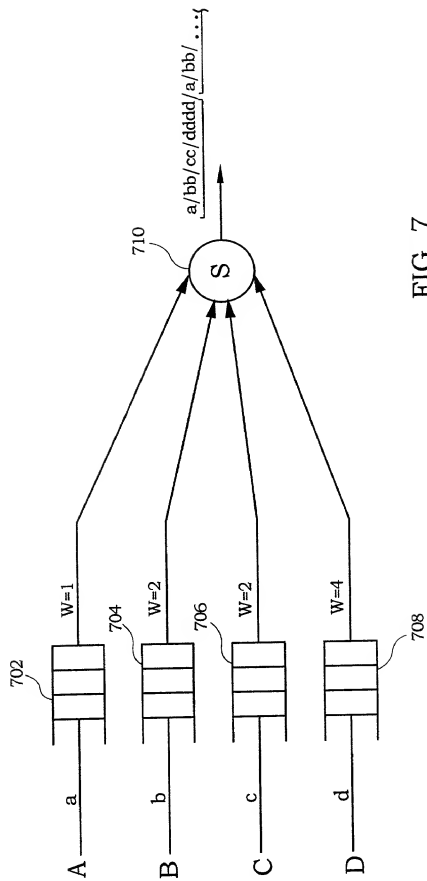


FIG. 7

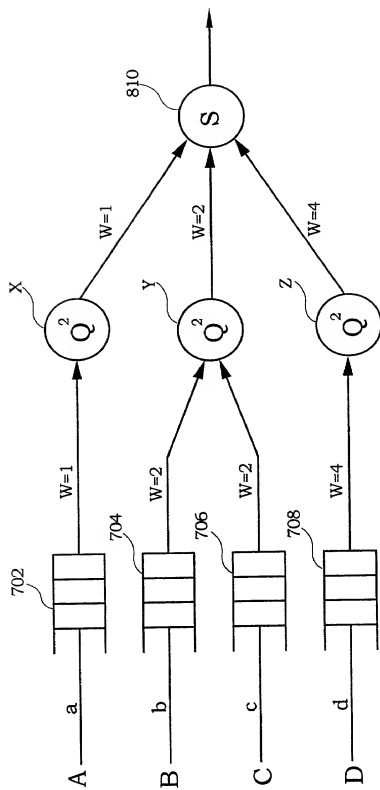


FIG. 8A

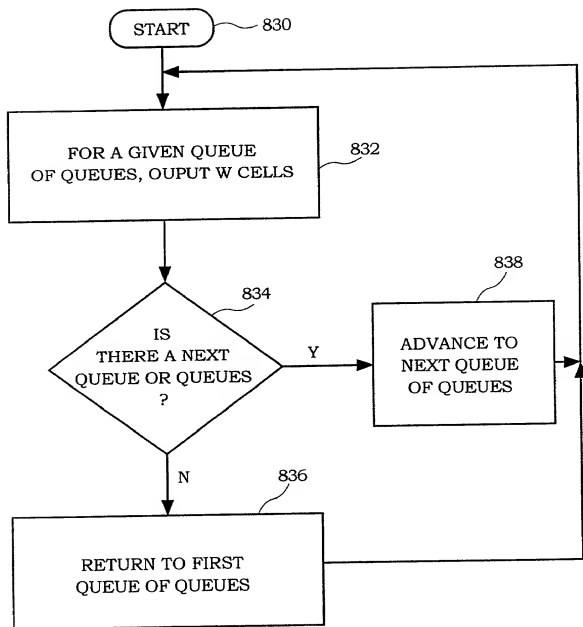


FIG. 8B

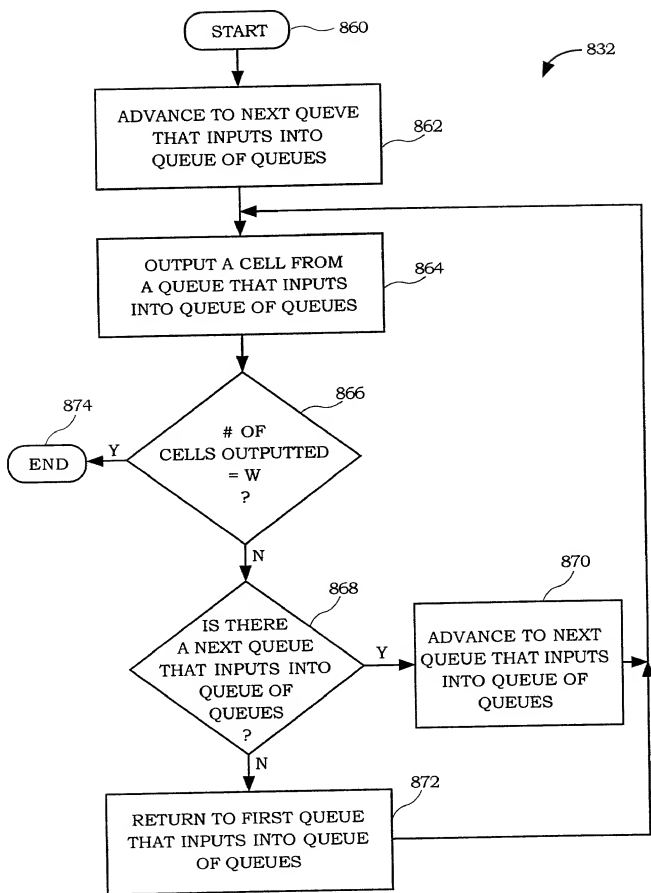


FIG. 8C

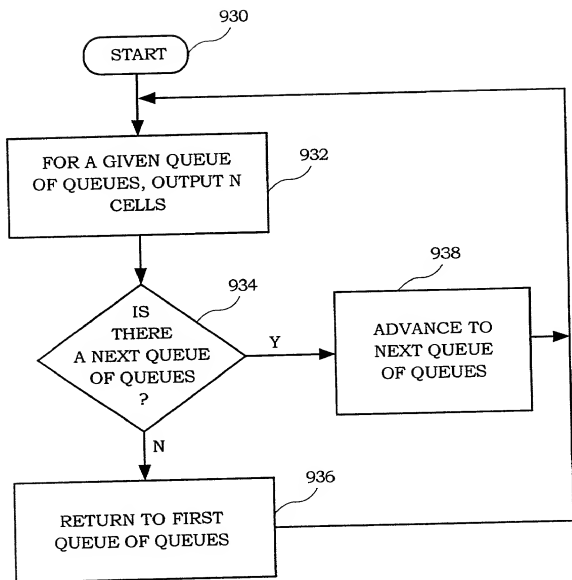


FIG. 9A

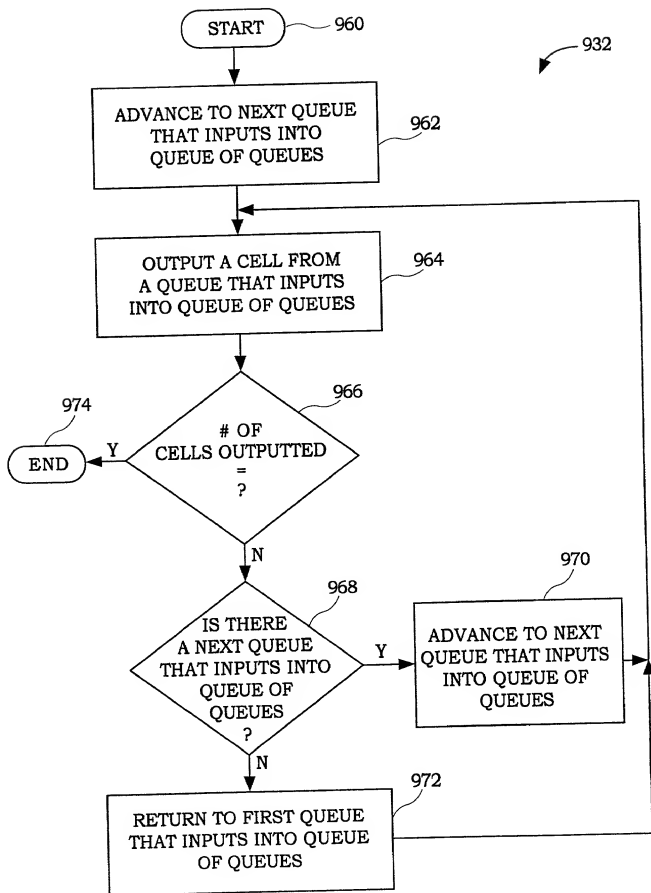


FIG. 9B

Queue of Queue	Static Weight	Dynamic Weight														Row #
		1	0	0	0	0	0	0	0	0	0	0	0	0	0	
X	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	
Y	2	2	2	1	1	0	0	0	0	2	1	1	0	0	0	
Z	4	4	4	4	3	3	2	1	0	4	4	3	3	2	1	
Output																
		X	Y	Z	Y	Z	Z	Z	Z	X	Y	Z	Y	Z	Z	
Column #																
		1	2	3	4	5	6	7	8	9	10	11	12	13	14	

FIG. 10

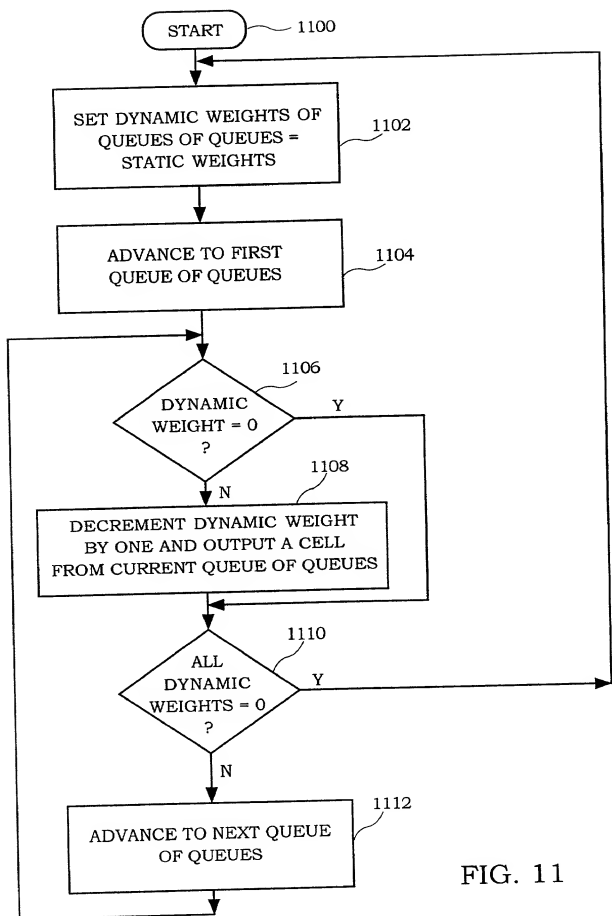


FIG. 11



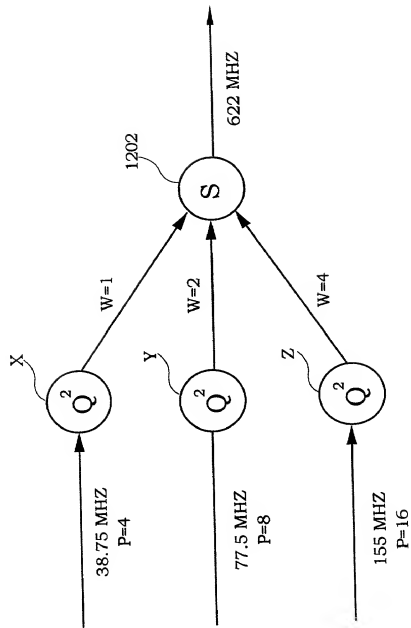


FIG. 12

Queue of Queue		Static Period Current Count																Row #																	
X	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	15	14	13																
Y	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0	7	6									
Z	3	2	1	0	3	2	1	0	3	2	1	0	3	2	1	0	3	2	1	0	3	2	1	0	3	2									
Output																																			
Column #		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34

FIG. 13

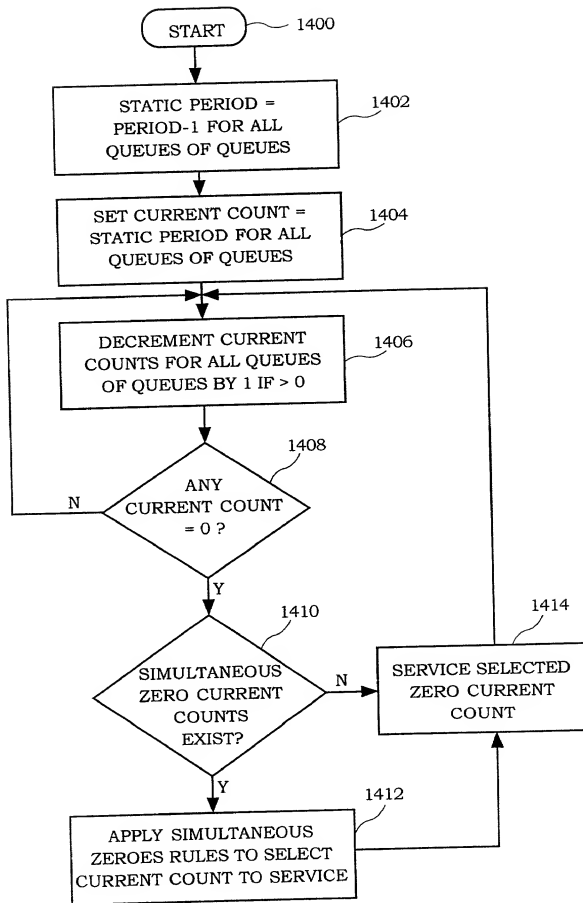


FIG. 14

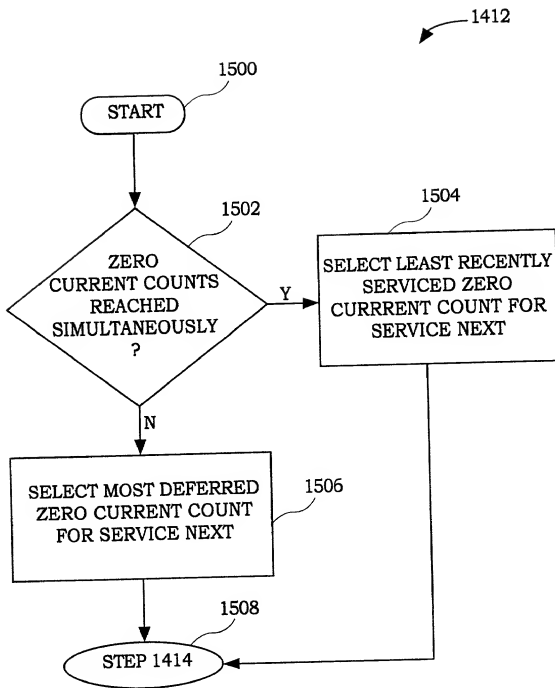


FIG. 15

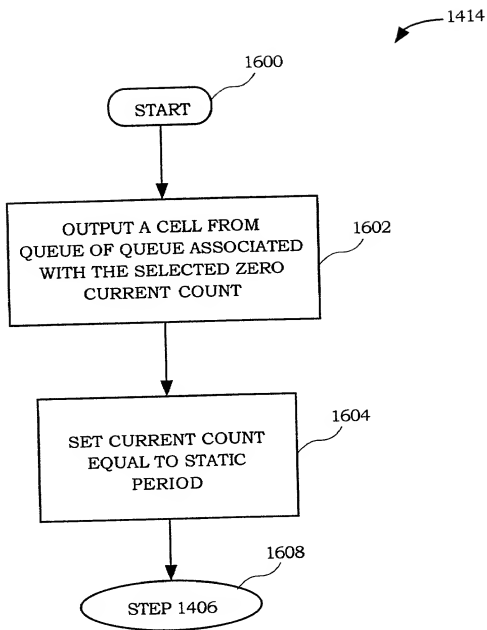


FIG. 16

